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(to be used for all correspondence after initial filing)

Application Number	10/766,131
Filing Date	January 28, 2004
First Named Inventor	Richard E. Perego
Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	RAMB-01049US2
Total Number of Pages in This Submission	

**ENCLOSURES (Check all that apply)**

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance communication to Group
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<input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53		

**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm or Individual name	Kirk J. DeNiro, Esq., Vierra Magen Marcus Harmon & DeNiro LLP
Signature	<i>KJR</i>
Date	February 24, 2004 2/24/04

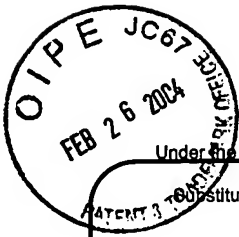
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Signature	<i>KJR</i>	Date February 24, 2004

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STATEMENT BY APPLICANT**

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Sheet 1 of 4

**Complete If Known**

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Art Unit	Unknown
Examiner Name	Unknown
Attorney Docket Number	RAMB-01049US2

**U. S. PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US-5,953,215	09-14-1999	Karabatsos	
		US-6,142,830	11-07-2000	Looeffler	
		US-6,185,644 B1	02-06-2001	Farmwald et al.	
		US-6,266,252 B1	07-24-2001	Karabatsos	
		US-6,317,352 B1	11-13-2001	Halbert et al.	
		US-6,414,904 B2	07-02-2002	So et al.	
		US-6,446,158 B1	09-03-2002	Karabatsos	
		US-6,493,250 B2	12-10-2002	Halbert et al.	
		US-6,530,006 B1	03-04-2003	Dodd et al.	
		US-6,643,752 B1	11-04-2003	Donnelly et al.	
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		US-5,701,313	12-23-1997	Purdham	
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		US-6,185,644	02-06-2001	Farmwald et al.	

**FOREIGN PATENT DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				

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Sheet 2

of 4

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Application Number	10/766,131
Filing Date	January 28, 2004
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Art Unit	Unknown
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Attorney Docket Number	RAMB-01049US2

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		Number-Kind Code <sup>2</sup> (if known)			
		US-5,034,917	07-23-1991	Bland et al.	
		US-4,977,498	12-11-1990	Rastegar et al.	
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		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				

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Substitute for form 1449B/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				<b>Complete If Known</b>	
				Application Number	10/766,131
				Filing Date	January 28, 2004
				First Named Inventor	Richard E. Perego
				Art Unit	Unknown
				Examiner Name	Unknown
Sheet	3	of	4	Attorney Docket Number	RAMB-01049US2

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Intel Developer Forum, "DDR2 Memory In 2004 Servers - Recipes For Successful Designs", September 2003	
		QBM Alliance, Platform Conference, "Quad Band Memory (QBM tm): DDR 200/266/333 devices producing DDR400/533/667, Jan. 23-24, 2002	
		KARABATSOS, C., "Quad Band Memory (QBM) Technology", Kentron Technologies, Inc., April 2001	
		Quad Band Memory (QBM), "The 'QBM Enabled' VIA PT880/PM880 Chipset Solutions with the Fastest Memory",	
		WEBER, F., "The AMD Athlon Processor: Future Directions", AMD	
		*"Intel 82804AA Memory Repeater Hub for SDRAM (MRH-S) Datasheet", Intel Corp., pp. 2-48 (Nov. 1999)	
		*"Intel 82805AA Memory Translator Hub (MTH) Datasheet", Intel Corp., pp. 1-48 (Nov. 1999)	
		*CATALDO, A., "TI Fields DSP-Based Media Processor on a DIMM", EE Times (Jan. 2000)	
		*MAC WILLIAMS, P., "PC Memory Directions For 1999 and Beyond", Intel Developer Forum, pp. 1-10 (Sep. 1998)	
		*JOHNSON, B., "Direct RDRAM Basic Architecture and Program Overview", Intel Developer Forum, pp. 1-14 (Sep. 1998)	

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Examiner Name	Unknown
Attorney Docket Number	RAMB-01049US2

Sheet 4 of 4

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		*GILLINGHAM, P., "SLDRAM Architectural and Functional Overview", SLDRAM Consortium, SLDRAM Inc., pp. 1-14 (Aug. 1997)	
		**Draft Standard for a High-Speed Memory Interface (Synclink)", Draft 0.99 IEEE P1596.7-199X, IEEE Inc., pp. 1-51 (1996)	
		*WIGGERS, H., "SyncLink A Proposal for an Implementation of IEEE P1596.4", Hewlett Packard, pp. 1-20 (Mar. 1995)	
		**KMM377S1620CT2 SDRAM Module Datasheet", Samsung Electronics Inc., pp. 1-2 (Nov. 1998)	
		**SLD4M18DR400 4MEG x 18 SLDRAM Datasheet", SLDRAM Inc., pp. 1-69 (July 1998)	
		*POULTON, J., "Signaling in High Performance Memory Systems", IEEE Solid State Circuits Conference, slides 1-59 (Feb. 1999)	
		**SLDRAM 400Mb/s/pin Command/Address Repeater", SLD10400 Rev .5, SLDRAM Consortium, pp. 1-12 (Jan. 7, 1998)	
		*Minutes of meeting no. 75 JEDEC JC-42.3 Committee on RAM Memories, and referenced attachment "Y" of the presentation titled "Synclink", May 24, 1995, New Orleans	
		Intel Developer Forum, "Fully Buffered DIMM (FB-DIMM) Server Memory Architecture: Capacity, Performance, Reliability, and Longevity", February 18, 2004	
		Intel Developer Forum, "Fully Buffered DIMM (FB-DIMM) Design Considerations", February 18, 2004	

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